

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4793	(memory adj3 module) same (volatile or DRAM or SRAM or RAM) same (non\$1volatile or EEPROM or ROM or flash)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:30
L2	13	L1 same (operation adj5 current\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:30
L3	623	(operation adj3 current\$4) same (volatile or DRAM or SRAM or RAM) same (non\$1volatile or EEPROM or ROM or flash)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:30
L4	21	1 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:30
L5	3364	365/189.01.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:31
L6	1568	365/207.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:33
L7	848	365/208.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:35
L8	13	3 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:35

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L9	13	3 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:35
L10	3	3 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:35
L12	11	1 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:35
L13	7	1 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:35
L14	3	1 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:35
L15	288	L1 same current\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:36
L16	47	15 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:36
L17	209	15 and @ad<"20021213"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:36

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L18	623	(operation adj3 current\$4) same (volatile or DRAM or SRAM or RAM) same (non\$1volatile or EEPROM or ROM or flash)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:37
L19	304	L18 same data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:37
L20	28	L19 and boot\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:37
L21	9924	memory same (amplifier\$3 or amplif\$3) same (read adj3 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:37
L22	364	L21 same bit\$1line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:37
L23	89	L22 same gat\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:00
L24	0	((semiconductor or memory) and (sense adj2 amplif\$3) and (read adj2 data) and (bit adj2 line adj3 pair) and (command adj2 signal) and (write adj2 data) and (operat\$3 adj3 control adj3 unit) and (turn adj2 on) and address\$3 and (turn adj2 off)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:04

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L25	0	((semiconductor or memory) and (sense adj2 amplif\$3) and (read adj2 data) and (bit adj2 line adj3 pair) and (command adj2 signal) and (write adj2 data) and (first adj2 transistor) and (second adj2 transistor) and (first adj3 sense adj3 transistor) and (second adj3 sense adj3 transistor) and (third adj3 sense adj3 transistor)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:06
L26	0	((semiconductor or memory) and (sense adj2 amplif\$3) and (read adj2 data) and (bit adj2 line adj3 pair) and (command adj2 signal) and (write adj2 data) and (first adj2 transistor) and (second adj2 transistor) and (first adj3 sense adj3 transistor) and (second adj3 sense adj3 transistor) and (third adj3 sense adj3 transistor) and (fourth adj3 sense adj3 transistor)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:07
L27	0	((semiconductor or memory) and (direct adj3 sense adj3 amplif\$3) and (first adj3 bit adj3 line) and (second adj3 bit adj3 line) and (first adj3 data) and (second adj3 data) and read\$3 and writ\$3 and operat\$3 and (data adj3 load\$3) and signal).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:09
L28	0	((semiconductor or memory) and (direct adj3 sense adj3 amplif\$3) and (bit adj3 line) and first and second and read and write and signal and transistor and operat\$3 and (write/read adj3 control\$5) and (block adj3 signal)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:12
L29	5	lee-seung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:12
L30	0	sim-jae.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:12

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L31	0	sim-jae-y.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:12
L32	0	jae-sim.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 12:12
S1	565	(operation adj3 current\$3) same (volatile or DRAM or SRAM or RAM) same (non\$1volatile or EEPROM or ROM or flash)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:30
S2	4264	(memory adj3 module) same (volatile or DRAM or SRAM or RAM) same (non\$1volatile or EEPROM or ROM or flash)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:29
S3	13	S2 same (operation adj5 current\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:30
S4	19	S1 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:27
S5	16	S4 not S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:27
S6	176	S2 same current	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:36

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S7	36	S6 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:35
S8	2	S6 and "365"/189.01.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:31
S9	13	S6 and "365"/185\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:32
S10	22	S7 not S8 not S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 15:36
S11	565	(operation adj3 current\$4) same (volatile or DRAM or SRAM or RAM) same (non\$1volatile or EEPROM or ROM or flash)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:51
S12	266	S11 same data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:29
S13	3	S12 same boot\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:30
S14	21	S12 and boot\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:37

EAST Search History

S15	19	S14 and value	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:32
S16	106	S12 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:37
S17	88	S16 and @ad<"20040401"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:58
S18	3976	memory same data same (operat\$3 adj3 current\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:44
S19	9	S18 same unique\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:45
S20	551	S18 same speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:45
S21	41	S20 same values\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:45
S22	4	S11 and 713/1.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:52

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S23	12	S11 and "713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:56
S24	8	(operat\$3 adj2 current\$3) near5 (access\$3 adj3 data\$1bas\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:57
S25	2	S24 same memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:57
S26	3	(operat\$3 adj2 current\$3) same (access\$3 adj3 data\$1bas\$2) same memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:58
S27	71	(operat\$3 adj2 current\$3) same (access\$3 adj3 data) same memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 14:58
S28	63	S27 and @ad<"20040401"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/29 15:16
S29	9345	memory same (amplifier\$3 or amplif\$3) same (read adj3 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11
S30	50	S29 same (bit\$1line adj3 pair\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 12:24

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S31	341	S29 same bit\$1line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 12:24
S32	4	S31 same (gate adj3 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 12:26
S33	84	S31 same gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/07 11:37
S34	75	S33 and @ad<"20021231"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
S35	69	S34 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11
S37	183	S29 same (gate adj3 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11
S38	132	S37 and @ad<"20021231"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11
S39	91	S38 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:12

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S40	88	S39 not S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:49
S41	3226	amplifier\$3 same (control adj3 gate) same read\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:50
S42	172	S41 same bit\$1in\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:50
S43	3	S42 same (read/write adj3 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:52
S44	5	S42 same read/write	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:53
S45	29	S42 same (input/output or I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:33
S46	27	S45 not S43 not S44	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:56
S47	82	S41 and 365/206,207,208.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32

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S48	576	S29 and 365/206,207,208.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
S49	523	S48 and @ad<"20021231"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
S50	268	S49 and (input/output or I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:33
S51	233	S50 and read and write	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:33
S53	60	S51 and (control adj3 gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:34
S54	45	S53 not S47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:20
S55	0	(circuit and amplifier\$3 and (read adj3 data) and (bit\$1in\$1 adj2 pair\$1) and (read adj3 command) and (gate adj3 circuit) and (read/write adj2 signal)).clms.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:24
S56	5	lee-seung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:25

EAST Search History

S57	0	sim-jae.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:25
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